

ABSTRACT OF THE DISCLOSURE

A semiconductor circuit component has: an MOS-FET 12 which is a load-control semiconductor switching device with a control terminal; a control signal supply circuit 14 including a charging pump circuit for supplying a control signal to a gate G of the MOS-FET 12 to drive the MOS-FET 12; and a drive control circuit 16 for performing drive control only when a switch unit SW externally provided is turned on, so that a power supply voltage is supplied from a battery power supply to the control signal supply circuit 14 to thereby make the control signal supply circuit 14 output the control signal.

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